

INDEX

VOLUME 2: 1987

AUTHOR

Barra, J R, Becker, M, Kouka, E F and Tricot, M Application of data analysis methods and of simulated annealing for the automatic layout of circuits

Bassiouni, M A and Gupta, A Computing estimates of waiting times in ring local area networks with priority classes

Dowdy, L W and Gordon, K D Performance sensitivity with respect to the distribution of the multiprogramming level

Downs, T Reliability problems in software engineering — a review

Elmagarmid, A K Fault-tolerant deadlock detection in distributed database systems

Gait, J Synchronizing multiprocessor access to shared operating system data structures

Hong, Y-C and Payne, T H Efficient computation of dataflow graphs in a hypercube architecture

Hughes, H, Chuang, K R and Yegani, P A give-up based protocol for token passing

Hughes, H, Jiang, H and Smith, S Voice/data integrated transmission on a time division CSMA/CD network

Hull, M E C, Roulston, R G and Gregg, T R S Interactive computing service monitoring system

Hurson, A R and Miller, L L Database machine architecture for supporting incomplete information

Iyer, B R, Yu, P S and Donatiello, L Analysis of fault-tolerant multiprocessor architectures for lock engine design

Karatzas, H D Simulation study of multitasking of closed computer system networks

Son, S H On multiversion replication control in distributed systems

Varadharajan, V and Baker, K D Net-based system design representation and analysis

Wong, C Y, Dillon, T S and Forward, K E Concurrent, real-time systems: a systematic approach using timed Petri nets

Yak, Y W, Dillon, T S and Forward, K E Modelling the effect of transient faults in fault-tolerant computer systems

117
161

Net-based system design representation and analysis

Varadharajan, V and Baker, K D

On multiversion replication control in distributed systems

Son, S H

Performance sensitivity with respect to the distribution of the multiprogramming level

Dowdy, L W and Gordon, K D

Reliability problems in software engineering — a review

Downs, T

Simulation study of multitasking of closed computer system networks

Karatzas, H D

Synchronizing multiprocessor access to shared operating system data structures

Gait, J

Voice/data integrated transmission on a time division CSMA/CD network

Hughes, H, Jiang, H and Smith, S

167
76
131
42
186
125

TITLE

155

92

131

16

186

29

85

85

155

125

179

107

59

107

42

76

16

179

161

A give-up based protocol for token passing

Hughes, H, Chuang, K R and Yegani, P

Analysis of fault-tolerant multiprocessor architectures for lock engine design

Iyer, B R, Yu, P S and Donatiello, L

Application of data analysis methods and of simulated annealing for the automatic layout of circuits

Barra, J R, Becker, M, Kouka, E F and Tricot, M

Computing estimates of waiting times in ring local area networks with priority classes

Bassiouni, M A and Gupta, A

Concurrent, real-time systems: a systematic approach using timed Petri nets

Wong, C Y, Dillon, T S and Forward, K E

Database machine architecture for supporting incomplete information

Hurson, A R and Miller, L L

Efficient computation of dataflow graphs in a hypercube architecture

Hong, Y-C and Payne, T H

Fault-tolerant deadlock detection in distributed database systems

Elmagarmid, A K

Interactive computing service monitoring system

Hull, M E C, Roulston, R G and Gregg, T R S

Modelling the effect of transient faults in fault-tolerant computer systems

Yak, Y W, Dillon, T S and Forward, K E

85
59
85
59
3
117
107
16
179

SUBJECT

Book reviews

Parallel array processing

Quantitative system performance

The massively parallel processor

Calendar

56, 103, 152, 200

Conferences

Inaugural annual conference: UK Chapter, Computer Measurement (User) Group

54

News

99, 150, 198

Publications

Fault-tolerant computer systems

53

System description

193

INDEX

VOLUME 2: 1987

AUTHOR

Barra, J R, Becker, M, Kouka, E F and Tricot, M Application of data analysis methods and of simulated annealing for the automatic layout of circuits

Bassiouni, M A and Gupta, A Computing estimates of waiting times in ring local area networks with priority classes

Dowdy, L W and Gordon, K D Performance sensitivity with respect to the distribution of the multiprogramming level

Downs, T Reliability problems in software engineering — a review

Elmagarmid, A K Fault-tolerant deadlock detection in distributed database systems

Gait, J Synchronizing multiprocessor access to shared operating system data structures

Hong, Y-C and Payne, T H Efficient computation of dataflow graphs in a hypercube architecture

Hughes, H, Chuang, K R and Yegani, P A give-up based protocol for token passing

Hughes, H, Jiang, H and Smith, S Voice/data integrated transmission on a time division CSMA/CD network

Hull, M E C, Roulston, R G and Gregg, T R S Interactive computing service monitoring system

Hurson, A R and Miller, L L Database machine architecture for supporting incomplete information

Iyer, B R, Yu, P S and Donatiello, L Analysis of fault-tolerant multiprocessor architectures for lock engine design

Karatzas, H D Simulation study of multitasking of closed computer system networks

Son, S H On multiversion replication control in distributed systems

Varadharajan, V and Baker, K D Net-based system design representation and analysis

Wong, C Y, Dillon, T S and Forward, K E Concurrent, real-time systems: a systematic approach using timed Petri nets

Yak, Y W, Dillon, T S and Forward, K E Modelling the effect of transient faults in fault-tolerant computer systems

117

Net-based system design representation and analysis

Varadharajan, V and Baker, K D

167

On multiversion replication control in distributed systems

76

Performance sensitivity with respect to the distribution of the multiprogramming level

92

Dowdy, L W and Gordon, K D Reliability problems in software engineering — a review

Downs, T Simulation study of multitasking of closed computer system networks

131

Karatzas, H D Synchronizing multiprocessor access to shared operating system data structures

42

Gait, J 186

Voice/data integrated transmission on a time division CSMA/CD network

125

TITLE

155

92

131

16

186

29

85

155

125

179

107

59

42

76

16

107

29

179

161

A give-up based protocol for token passing

Hughes, H, Chuang, K R and Yegani, P

85

Analysis of fault-tolerant multiprocessor architectures for lock engine design

Iyer, B R, Yu, P S and Donatiello, L

59

Application of data analysis methods and of simulated annealing for the automatic layout of circuits

Barra, J R, Becker, M, Kouka, E F and Tricot, M

3

Computing estimates of waiting times in ring local area networks with priority classes

Bassiouni, M A and Gupta, A

155

Concurrent, real-time systems: a systematic approach using timed Petri nets

Wong, C Y, Dillon, T S and Forward, K E

117

Database machine architecture for supporting incomplete information

Hurson, A R and Miller, L L

107

Efficient computation of dataflow graphs in a hypercube architecture

Hong, Y-C and Payne, T H

29

Fault-tolerant deadlock detection in distributed database systems

Elmagarmid, A K

16

Interactive computing service monitoring system

Hull, M E C, Roulston, R G and Gregg, T R S

179

Modelling the effect of transient faults in fault-tolerant computer systems

Yak, Y W, Dillon, T S and Forward, K E

161

BOOK REVIEWS

Parallel array processing

149

Quantitative system performance

101

The massively parallel processor

52

CALENDAR

56, 103, 152, 200

CONFERENCES

Inaugural annual conference: UK Chapter, Computer Measurement (User) Group

54

NEWS

99, 150, 198

PUBLICATIONS

Fault-tolerant computer systems

53

SYSTEM DESCRIPTION

193

